

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

		Group Art Unit: 1762 Examiner: To be assigned
In Re PATEN	T APPLICATION Of:	
Applicant(s):	Hiroomi TSUTAE	)
Serial No.:	10/657,207	)
Filed:	September 9, 2003	STATUS REQUEST ) ) )
For:	Process of cleaning a semiconductor manufacturing system and method of manufacturing a semiconductor device	
Attorney Ref:	FUJI 136	)
Commissioner P.O. Box 1450 Alexandria, V.		
Sir:	·	
Please	let us know the status of the above-identified	ed application and when an
Action may be	expected. If the response to this status req	uest is made by telephone, please

Respectfully submitted,

January 30, 2006

as for Tonya Harris (Docket Clerk) at extension 518.

Date

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